

RESUME

Mrs. Shree Lakshmi G

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Professional Summary

Seasoned Assistant Professor with 10+ Years of experience in teaching for undergraduate courses in Electronics and Communication Engineering. Actively participate in continued learning through conferences and professional workshops. Believe in students' abilities to learn and inherent thirst for knowledge with the right environment. Actively work to connect students to their material to transform lives.

Skills

- Strong critical and analytical skills with reading, writing, and comprehension
- Passion for connecting students to learning modalities that incite their interest in the Humanities
- Skilled at explaining and explicating material in a manner that students of varying levels can digest
- Pleasant personality to connect with students and colleagues
- Voracious appetite for increased knowledge

Work Experience

Assistant Professor at RNS Institute of Technology, Bangalore (April 2022 – May 2025)

- Subjects handled during my course of teaching are Network Security, Introduction to Basic Electronics, Technological Innovation Management and Entrepreneurship, Introduction to C programming, RM and IPR, VLSI Lab, ARM Lab, Micro-controller Lab, Computer networks Lab, Analog electronics and digital electronics lab.
- Member of National Conference Organising Committee in 2022 and 2023 held at RNSIT
- Create curricula in accordance with departmental standards.
- Deliver lectures and facilitate classroom discussions on respective subjects.
- Regularly meet with students during office hours to address concerns and offer feedback. Provide extra assistance to students as needed – Mentoring students.
- Technical Seminar Co-ordinator.
- Participate in campus events, technical and cultural events.
- Keep thorough records of student scores and attendance.
- Incharge of Department Library.
- Incharge of CO-PO Attainment (NBA Documentation)

Assistant Professor at NIE Institute of Technology, Mysore (August 2010 – December 2017)

- Subjects handled during my course of teaching are Fundamentals of CMOS VLSI, Operating Systems, Analog Electronics Circuits, Control Systems, Computer Communication Networks, RTOS, GSM, Microprocessor, Basic Electronics, Digital Electronics, Electronics Instrumentation, Principles of Communication System, and Digital Communication to undergraduate students.
- Prepare reading materials, tests, and quizzes.
- Create curricula in accordance with departmental standards.
- Deliver lectures and facilitate classroom discussions on respective subjects.
- Regularly meet with students during office hours to address concerns and offer feedback.
- Provide letters of recommendation and other referrals to students pursuing further programs.
- Participate in campus events.
- Keep thorough records of student scores and attendance.
- Provide extra assistance to students as needed.

Project Trainee at Honeywell Technology Solutions Lab Pvt. Ltd, Bangalore (September 2009 – April 2010)

- Design of Simulink Library from code base for motor control applications
- Domain: Aerospace - Electrical Power System

Participation in Workshops/FDPs

- 3 Days FDP on Inculcating Universal Human Values in Technical Education organised by AICTE at RNSIT from 30th Nov to 2nd Dec 2023.
- Workshop on VLSI to System Design: Silicon to End Application Approach organised by AICTE from July 31st to August 4th 2023.
- Workshop on Intellectual Property Rights organised by RNSIT and KSCST on July 22nd 2023.

Personal Profile

Name	Mrs. Shree Lakshmi G
Spouse Name	Mr. Vijeth S
Father's Name	L Giridhar
Date of Birth	8th June 1986
Marital Status	Married
Nationality	Indian
Languages known	English, Hindi and Kannada
Address:	No. 236/A, Venkatenahalli Street, NPO Road Cross, Chickballapur-562101

Educational Qualifications

Qualification	Institute/University	Percentage	Year
B.E. (Electronics & Communications)	VidyaVikas Institute Of Engineering and Technology, Mysore.(VTU)	73.53%	2008
M.Tech (VLSI Design & Embedded Systems)	PES College of Engineering, Mandya. (VTU)	7.99 CGPA	2010
Ph.D	Cleared Entrance Test at VTU -2023, yet to register.		

Academic Projects

M.Tech	
Title of M. Tech Dissertation	“FPGA based System On Chip (SOC) design, verification and implementation”
Guidance	Dr. V Sridhar, Principal, PESCE, Mandya, and Mr. S V Nagendra, Concoct Electronics, Bangalore.
Area	Network Communication using Verilog HDL
B.E.	
Title of B.E. Dissertation	“Kannada Optical Character Recognition System”
Guidance	Mrs.Bindu A. Thomas, Asst professor, dept of E&C, VVIET, Mysore.
Area	Image processing using MATLAB
Other Projects	
Title of B.E. Dissertation	“Implementation Of Hamming Code For Error Detection And Correction Using VHDL”
Guidance	Mr.Anand.M.J. Lecturer, ECE, VVIET, Mysore
Area	Network Communication using VHDL

I declare that the above given information is true to the best of my knowledge.

Date:

Place:

SHREE LAKSHMI G